IN THE CLAIMS:

Please amend the claims 1 and 7 as follows:

1. (Currently Amended) A clock generating method for an asynchronous transmission, comprising:

determining a plurality of actual signal arrival times for a number of samples;

averaging the plurality of actual signal arrival times over the number of samples;

correcting a timing of a receiving clock on a basis of an average of the plurality of actual signal arrival times and an expected signal arrival time;

deriving an expected signal arrival time from the receiving clock; and

determining a frequency difference between a frequency corresponding to an average of the plurality of actual signal arrival times and a frequency of said receiving clock, and changing the frequency of the receiving clock according to said frequency difference;

wherein the number of samples is set such that a time-dependent cell delay variation of actual signals being asynchronously transmitted has a mean value of zero.

- 2. (Canceled)
- 3. (Previously Presented) The method according to claim 1, wherein determining further comprises counting a time period between arrival of a first signal and arrival of a subsequent second signal.
- 4. (Previously Presented) The method according to claim 3, wherein averaging further comprises storing counted time periods and calculating an average of stored time periods.
 - 5. (Canceled)

- 6. (Previously Presented) The method according to claim 1, wherein the asynchronous transmission is an ATM transmission and the signal is an ATM cell.
- 7. (Currently Amended) A clock generating apparatus for asynchronous transmission comprising:

means for determining an average of actual signal arrival times over a number of samples and for generating a control signal on a basis of a determined average of the actual signal arrival times and an expected signal arrival time; and

means for correcting a timing of a receiving clock on a basis of the control signal;

wherein the number of samples is set such that a time-dependent cell delay variation of actual signals being asynchronously transmitted has a mean value of zero.

- 8. (Previously Presented) The apparatus according to claim 7, wherein the means for correcting comprises a voltage controlled oscillator.
- 9. (Previously Presented) The apparatus according to claim 7, wherein the means for determining comprises:

means for detecting an actual arrival time of a signal;

means for averaging a plurality of detected actual signal arrival times over the number of samples in order to obtain an average of the actual signal arrival times; and

means for comparing and correction control the average of the actual signal arrival times with the expected signal arrival time and for generating the control signal in accordance with a comparison result, wherein the expected signal arrival time is derived from the receiving clock.

- 10. (Previously Presented) The apparatus according to claim 9, wherein the means for determining comprises means for storing a plurality of detected actual signal arrival times.
- 11. (Previously Presented) The apparatus according to claim 9, wherein the means for detecting comprises a timer.
- 12. (Previously Presented) The apparatus according to claim 9, wherein means for comparing and correction control comprises a phase detector, and wherein a polarity of the control signal is changed in accordance with a result of comparison.
- 13. (Previously Presented) The apparatus according to claim 7, wherein the asynchronous transmission is an ATM transmission and the signal is an ATM cell.